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U.S.S.N. 10/707,348

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137275XZ (GEMS 0225 PA)

In the specification:

Please amend the specification as follows:

Referring now to Figure 2, a schematic and block diagrammatic [0027] view of an x-ray detector panel 50 having split scan lines 52 and non-split and combined data lines 54, in accordance with an embodiment of the present invention is shown. The detector 50 includes pixels, represented by and each of which having respective field effect transistors (FET) 56 and photodiodes 58. The scan lines 52 are used to activate the FETs 56 and to simultaneously allow the photodiodes 58 within a particular row segment to charge. Each split scan line has multiple separated activation lines, such as the activation lines 57. The data lines 54 are used to charge the photodiodes 58, thereby collecting exposure or offset data therefrom. The data lines 54 are used by the readout circuitry 30 to restore charge to the photodiodes 58. As each scan line 52 is activated, the photodiodes, within the pixels on the activated scan line, are simultaneously restored to an initial charge. Each data line 54 has an associated readout channel (not shown) from which the acquisition processing circuit 34 receives the exposure data.